

**PROFORMA FOR MANDATORY DISCLOSURE OF INFORMATION ABOUT ACCREDITED PROGRAMMES BY INSTITUTIONS TO BE INCLUDED IN THEIR RESPECTIVE INFORMATION BROCHURE, DISPLAYED ON THEIR WEBSITE AND TO BE SUBMITTED TO NBA EVERY YEAR WELL BEFORE THE ADMISSION PROCESS**

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The following information is to be published in the Information Brochure besides being hosted on the Institution's official Website as fixed content.

**Name of the Programme** : *B.E.(Electronics and Comm Engg)*

**Approved Intake** : *120(UG)*

**Fees charged (Per annum) in Rs.** : *Cat-I Rs 45,000=00(UG)*  
*Cat II Rs 1,60,000=00(UG)*

**Pass percentage during the last 5 year s:** *Annexure –1*

**Teacher Student Ratio** : *1:16(UG)*

**No. of Faculty members** :

<b>Cadre</b>	<b>Number</b>
Professor	: 01
Assoc. Prof	: 06
Asst.Prof	: 09
Lect	: 01

No. of faculty allocated to 1<sup>st</sup> year-02

**Faculty Profile** :

<b>Qualification</b>	<b>No. of faculty</b>
Post Graduate	: 17

**Experience of the faculty** :

<b>Number of faculty with</b>	
Less than five years	: 04
5 to 10 years	: 05
More than 10 years	: 08

**Details of individual faculty members** :

Name, Photograph, Date of Birth,  
Qualifications, Professional experience, Research  
Interests

*(Annexure –2)*

**Industrial Consultancy, research and Development (During the past 5 years)**

Amount of Industrial Consultancy earned (In lakhs of Rupees) :--NIL--

No. of sponsored research projects completed : --Nil--

No. of patents produced :--Nil--

No. of sponsored research projects in progress : --NIL--

No. of papers presented in International Conference / seminars : **43**

No. of papers presented in National Conference / seminars :

No. of International Conferences conducted :--Nil--

No. of national Conferences conducted : **05**

No. of Training Programmes / Short Term Courses conducted : **20**

**Placement Record for the previous year** :

No. of Companies who visited the campus :**75**

Names and Details of companies who visited the campus along with No. of students placed :**Annexure-3**

No. of students placed through campus recruitment :**90**

Maximum salary drawn (in Rs.) : **Rs 4,00,000=00 p.a**

Average salary drawn (in Rs.) : **Rs.3,00,000=00 p.a**

**Budget Allocation for the Programme (in Rs.)** :

**Recurring** :**Rs.1,00,000.00**

**Non-Recurring** :







**New initiatives and future vision, if any** :--Annexure-4--







K. L. E. SOCIETY'S  
**COLLEGE OF ENGINEERING AND TECHNOLOGY**  
 BELGAUM – 590 008  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**




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DETAILS OF RESULTS FOR THE PAST FIVE YEARS (UG, I-VIII Sem.)

Year	Semester	% of passing	Year	Semester	% of passing
Jan 2010	I	76.40	July 2010	II	91
	III	75.73		IV	73.53
	V	67.92		VI	72.38
	VII	92.59		VIII	91.84
Jan2011	I	72.29	July 2011	II	91
	III	72.73		IV	69.39
	V	73.47		VI	72.38
	VII	84.11		VIII	87.85
Jan 2012	I	92.47	July 2012	II	75
	III	63.06		IV	74.53
	V	75.76		VI	82.42
	VII	87.39		VIII	92.79
Jan 2013	I	88	July 2013	II	87
	III	62.61		IV	60.53
	V	80.19		VI	89.62
	VII	82.98		VIII	96.81
Jan 2014	I	76.19	July 2014	II	90.36
	III	72.31		IV	71.43
	V	72.07		VI	79.61
	VII	90.38		VIII	99.05
Jan 2015	I	65.00	July 2015		
	III	52.05			
	V	78.81			
	VII	93.62			

<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : S.B.Kulkarni  <b>Date of Birth</b> : 30-04-1964  <b>Highest Qualifications</b> : M. Tech.  <b>Professional Experience</b> : 27 Yrs ( Total)  <b>Industrial Exp.</b> :  <b>Teaching</b> : 27Yrs  <b>Research Interest</b> :</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : H.P. Rajani  <b>Date of Birth</b> : 25/03/1963  <b>Highest Qualifications</b> : M. Tech.  <b>Professional Experience</b> : 26 Yrs ( Total)  <b>Industrial Exp.</b> :  <b>Teaching</b> : 26Yrs  <b>Research Interest</b> :</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : G. P. KADAM  <b>Date of Birth</b> : 04-09-1963  <b>Highest Qualifications</b> : M. Tech. (Digital Electronics &amp; Communication Systems )  <b>Professional Experience</b> : 27Yrs (Teaching)  <b>Research Interest</b> : Digital Signal and hybrid processors</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Sheela Kiran. Kore  <b>Date of Birth</b> : 05-01-1964  <b>Highest Qualifications</b> : M. S. (Electronics &amp; Control)  <b>Professional Experience</b> :25Yrs (Teaching )  <b>Research Interest</b> : Image Processing</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Uma. Kulkarni  <b>Date of Birth</b> : 10-08-1970  <b>Highest Qualifications</b> : M.Tech. (Microelectronics &amp; Controls Systems)  <b>Professional Experience</b> : 17 Years ( Total)  <b>Industry</b> : 05 Yrs  <b>Teaching</b> : 12 Yrs  <b>Research Interest</b> : VLSI &amp; Embedded Systems and control systems</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Nataraj A. Vijapur  <b>Date of Birth</b> : 08-07-1979  <b>Highest Qualifications</b> : M.Tech. (VLSI Design &amp; Embedded Systems)  <b>Professional Experience</b> : 11 Years (Teaching )  <b>Research Interest</b> : VLSI and Emb.Systems</p>	

<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Arun Tigadi  <b>Highest Qualifications</b> : M.Tech(VLSI Design &amp; Embedded Systems)  <b>Date of Birth</b> : 31-01-1984  <b>Professional Experience</b> : 03.8 Years (<b>Teaching</b>)  <b>Research Interest</b> : VLSI ,Low power VLSI ,Memory design and Full custom design.</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Suresh Murugod  <b>Highest Qualifications</b> : M.Tech (Electronics Engineering)  <b>Date of Birth</b> : 31-10-1980  <b>Professional Experience</b> : 6.5 Years (<b>Teaching</b>)  <b>Research Interest</b> : DSP &amp; FPGA Programming</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Basavaraj Chougula  <b>Highest Qualifications</b> : M.Tech (VLSI Design.&amp; Embd.Sys)  <b>Date of Birth</b> : 13-09-1982  <b>Professional Experience</b> : 3.5 Years (<b>Teaching</b>)  <b>Research Interest</b> : VLSI</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Sonam Kandalgaonkar  <b>Highest Qualifications</b> : M.Tech (VLSI Design.&amp; Embd.Sys)  <b>Date of Birth</b> : 05-06-1988  <b>Professional Experience</b> : 06 Months (<b>Teaching</b>)  <b>Research Interest</b> : VLSI &amp; Embedded</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Prabhakar S Manage  <b>Highest Qualifications</b> : M.Tech (Electronics)  <b>Date of Birth</b> : 31-01-1987  <b>Professional Experience</b> : 02 ( <b>Teaching</b>)  <b>Research Interest</b> : DSP, Signal Processing, Control Systems</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Rudrappa Gujanatti  <b>Highest Qualifications</b> : M.Tech (Digital Communication and Networking)  <b>Date of Birth</b> : 12-07-1985  <b>Professional Experience</b> : 3.2 (<b>Teaching</b>)  <b>Research Interest</b> : Networking</p>	

<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Vijay B Rayar  <b>Highest Qualifications</b> : M.Tech (VLSI Design &amp; Embedded Systems)  <b>Date of Birth</b> : 30-06-1986  <b>Professional Experience</b> : 02 (<b>Teaching</b>)  <b>Research Interest</b> : VLSI Design</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Swati Mavinkattimath  <b>Highest Qualifications</b> : M.Tech  <b>Date of Birth</b> : 02-05-88  <b>Professional Experience</b> : 3.5 (<b>Teaching</b>)  <b>Research Interest</b> : VLSI &amp; Embedded</p>	
<p style="text-align: center;"><b><u>FACULTY-PROFILE</u></b></p> <p><b>Name</b> : Anand G  <b>Highest Qualifications</b> : M.Tech  <b>Date of Birth</b> :  <b>Professional Experience</b> : 08 (<b>Teaching</b>)  <b>Research Interest</b> : VLSI &amp; Embedded</p>	

## PLACEMENT FOR THE YEAR 2013-2014

Sl. No	Company Name	Total
1	Alcatel Lucent	1
2	AVH Chemicals	3
3	BMM Steel	3
4	e-Clerx	6
5	Electro systems	1
6	Globaledge Software	6
7	HP	1
8	IBM Global	5
9	Peol Solutions	4
10	Persistent Systems	3
11	Quest Global	6
12	Schiller India	1
13	Seimens	1
14	St. Jude	3
15	Tata Motors	6
16	TESCO	12
17	Young Brain india	2
18	Tata Consultancy Services	72
19	Cruiseline Shipping	17
20	Cognizant	2
21	Spectrum Planning Services	21
22	Torry Harris Business Solutions	1
23	FACE	17
24	Webonise	1
25	Tatatele Services	1
26	Fairfeild Atlas	3
27	MCF	1
28	EHSC	3
29	Quest Manufacturing	5
30	Mphasis	19
31	Panacea Biotech	1
32	JK Cements	1
33	AJ.Gallagher & Co	3
34	TAJ Hotels	1
35	Bagalkot Cement	8
36	Riddhi Siddhi Gluco Boils	1
37	Shriram Finance	7
38	Secon	0
39	NTT Data	6
40	Triveni Turbines	1
41	Robert Bosch	1
42	iGate	1
43	Aiccel Solutions	6

44	Phoneix	44
45	Online Instruments	2
46	Faurecia Interior Systems	2
47	ICICI Prudential	4
48	Navya Biotech	1
49	BBM Accoustic	2
50	Bosch Automotive	2
51	ANZ	1
52	ICICI Bank	2
53	Axis Bank	6
54	Infosys Technologies	14
55	Shoppers Stop	1
56	Pronet Informatix	4
57	Deshpande Foundation	2
58	Health Mantra	5
59	Creantic Solutions	1
60	Rahul Deshpande Associates	6
61	RetailOn	2
62	Positron	1
63	Aditya Birla Minacs	8
64	Competant Systems	2
65	MBECE Ambejogai	4
66	Metro	1
67	Praj Consultancy	1
68	Bermer	2
69	Conceptcity	1
70	Gharda	2
71	Dixit Consulting	6
72	Syntel	3
73	Unisys	3
74	Excel Health	1
75	Jacobs Structural	5
76	Practo Technologies	6
		0
		0
	<b><i>Total</i></b>	<b>411</b>



## **VISION**

To be an excellent education and research center creating high quality engineering professionals in Electronics and Communication Engineering.

## **MISSION**

To achieve excellence by executing the academic process with a vibrant student-teacher participation.

To promote global, economic, environmental and socially relevant research and developmental activities.

To groom emerging engineering professionals with high moral and ethical standards.

To encourage lifelong learning in developing innovative products, technologies and services.

To maintain consistent relationship with industry, government, society and alumni.

